

```

Old CPLD-Interface to Mergen:
0x1c08000 0x1  CMD0
0x1c08000 0x2  EN
0x1c08000 0x4  TDO (at CPLD)
0x1c08000 0x8  CMD1
0x1c08000 0x10 CMD2
0x1c08000 0x20 CMD3
0x1c08000 0x40 RET_0
0x1c08000 0x80 clock
0x1c08000 0x100 TCK
0x1c08000 0x200 TDI (at CPLD)
0x1c08000 0x400 CMD4
0x1c08000 0x800 TMS
0x1c08000 0x1000 RET_1

```

```

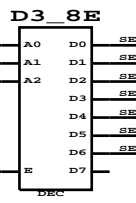
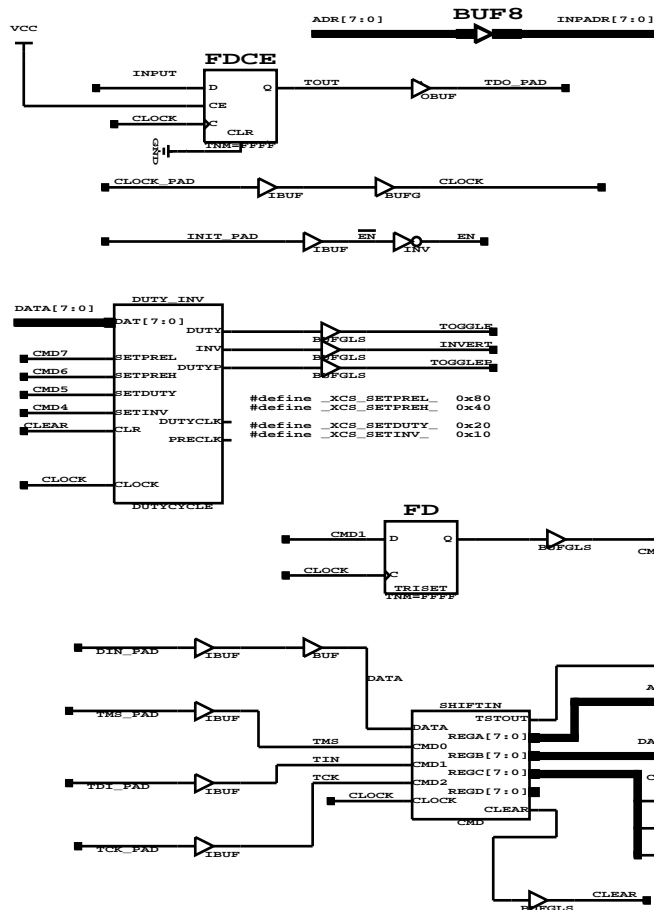
Interface to Mergen:
#define XCS_INIT 0x0001
#define XCS_CCLK 0x0004
#define XCS_DIN 0x0008
#define XCS_TMS 0x0010
#define XCS_TDI 0x0020
#define XCS_TCK 0x0040
#define XCS_TDO 0x0080
#define XCS_GCKS_ 0x0100

```

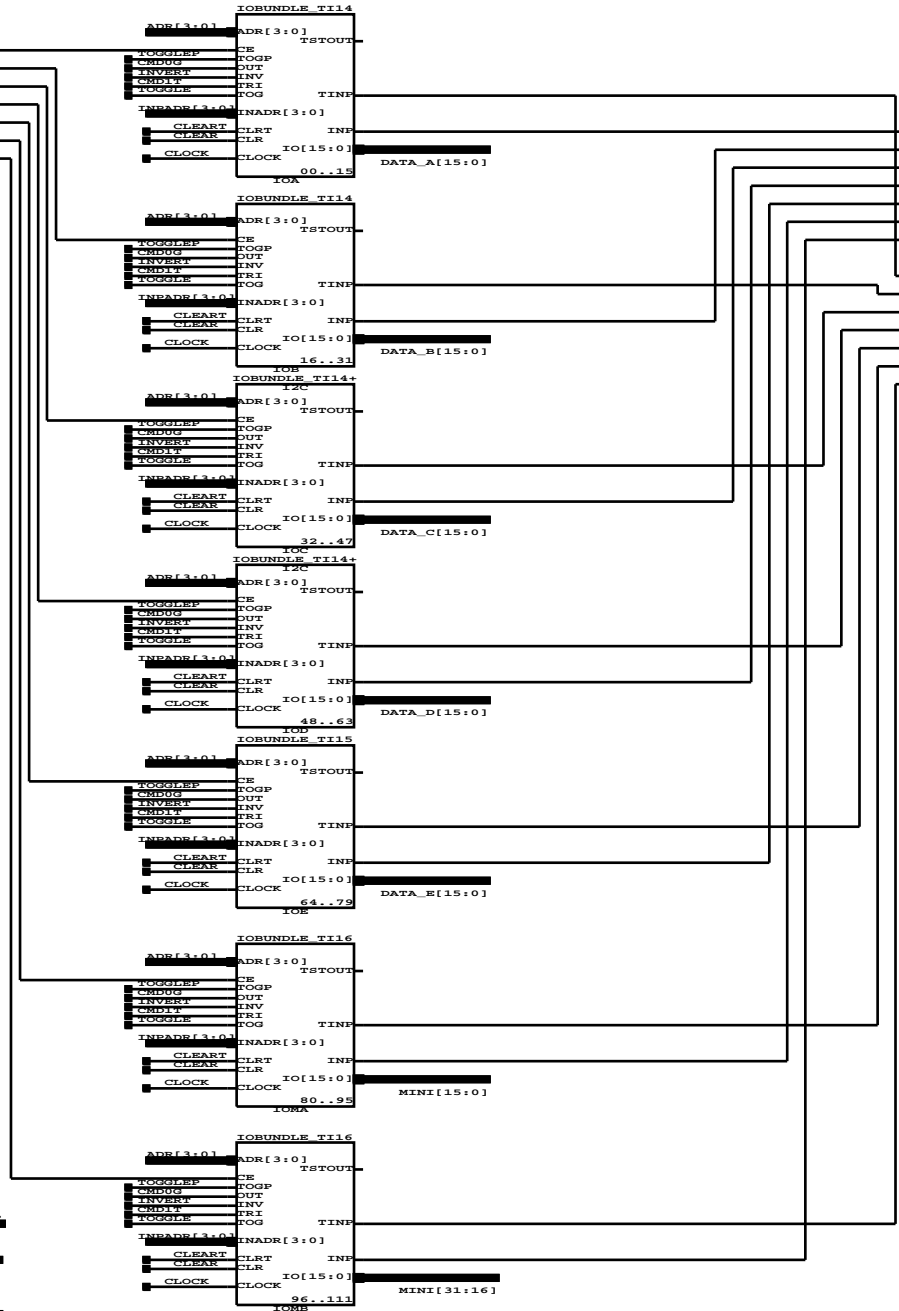
```

#define BIOPRO_CMD_OUT 0x01
#define BIOPRO_CMD_TRI 0x02
#define BIOPRO_CMD_SETIO 0x04
#define XCS_SETBIT0_ 0x06
#define XCS_SETBIT1 0x07
#define XCS_SETBITZ_ 0x05 OUT=H when Tristate
#define XCS_SETNOP_ 0x00
#define XCS_WR_ADDR_ (BIOPRO_WR_REG_A << 4)
#define XCS_RD_ADDR_ (BIOPRO_WR_REG_A << 4)
#define XCS_CMD_ADDR_ (BIOPRO_WR_REG_C << 4)
#define XCS_CLR_ADDR_ (BIOPRO_CLR_OP << 4)
#define XCS_WCP_ADDR_ (BIOPRO_NO_OP << 4)
#define XCS_ADR_TRISTATE 0x80
#define XCS_WR_DAT_ (BIOPRO_WR_REG_B << 4)

```



SETIO[6:0]



RDTPIN[6:0]

RDPPIN[6:0]

M16_1E

INPUT

```

#define XCS_VER_0 0xF0
#define XCS_VER_1 0xF1
#define XCS_VER_2 0xF2
#define XCS_VER_3 0xF3
#define XCS_VER_4 0xF4
#define XCS_VER_5 0xF5
#define XCS_VER_6 0xF6
#define XCS_VER_7 0xF7

```

VERSION[14:0]

M16_1E

TIMESPEC	
FFFF-FROM:FF:TO:FF-100NS	
105F000:104:TO:FF-100NS	